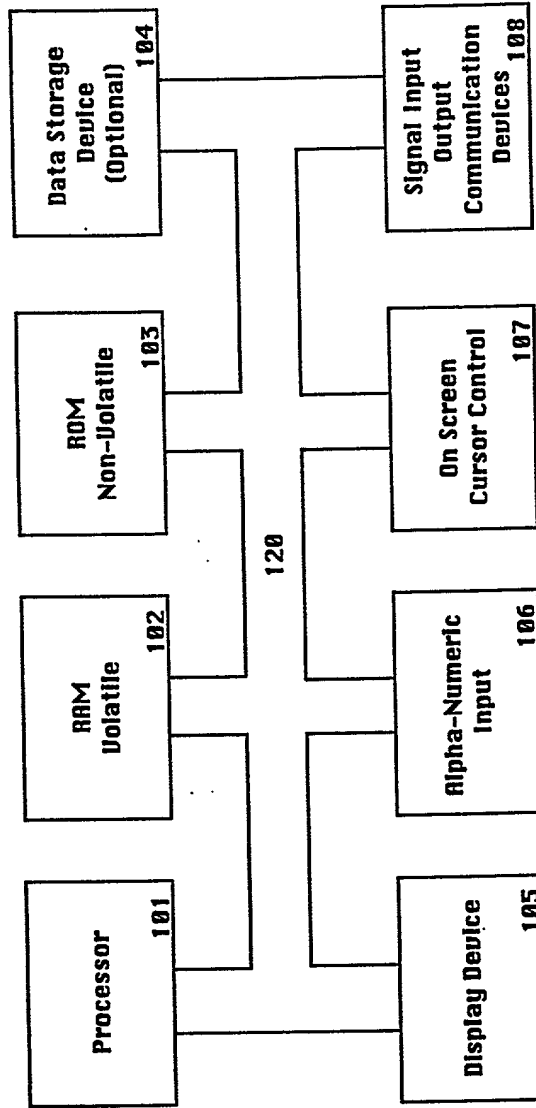
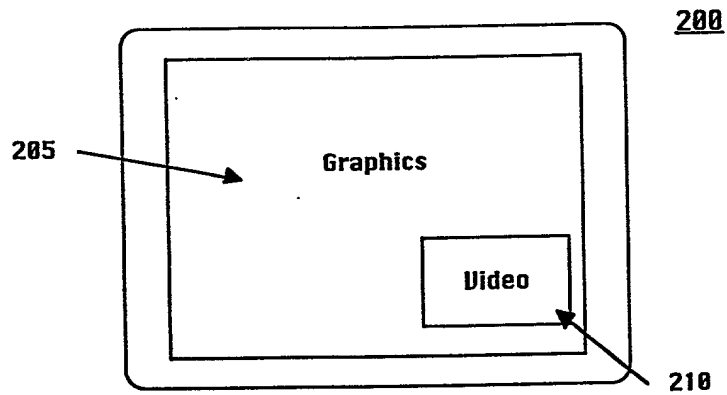


FIG. 1 is a block diagram of a computer system 100. The system 100 includes a processor 101, a display device 105, an alpha-numeric input 106, an on-screen cursor control 107, signal input/output communication devices 108, a data storage device (optional) 104, a RAM volatile 102, and a ROM non-volatile 103. The processor 101 is connected to the display device 105, the alpha-numeric input 106, the on-screen cursor control 107, the signal input/output communication devices 108, the data storage device 104, the RAM volatile 102, and the ROM non-volatile 103. The RAM volatile 102 and the ROM non-volatile 103 are connected to the data storage device 104. The alpha-numeric input 106 and the on-screen cursor control 107 are connected to the signal input/output communication devices 108.

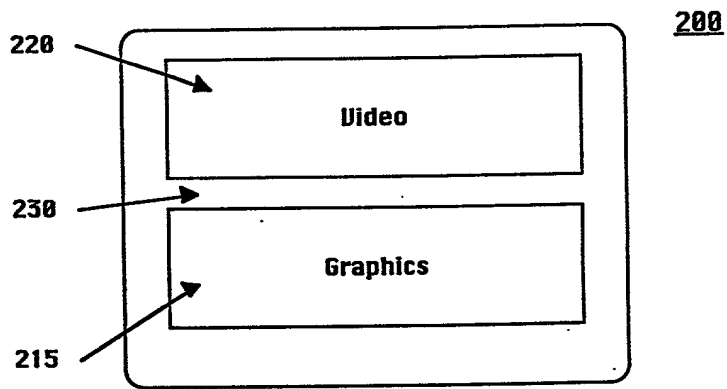
**100**



**Fig. 1**

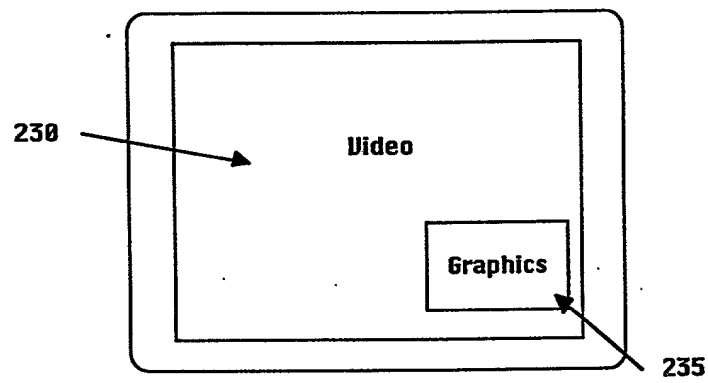


**Fig. 2a**



**Fig. 2b**

200



**Fig. 2c**

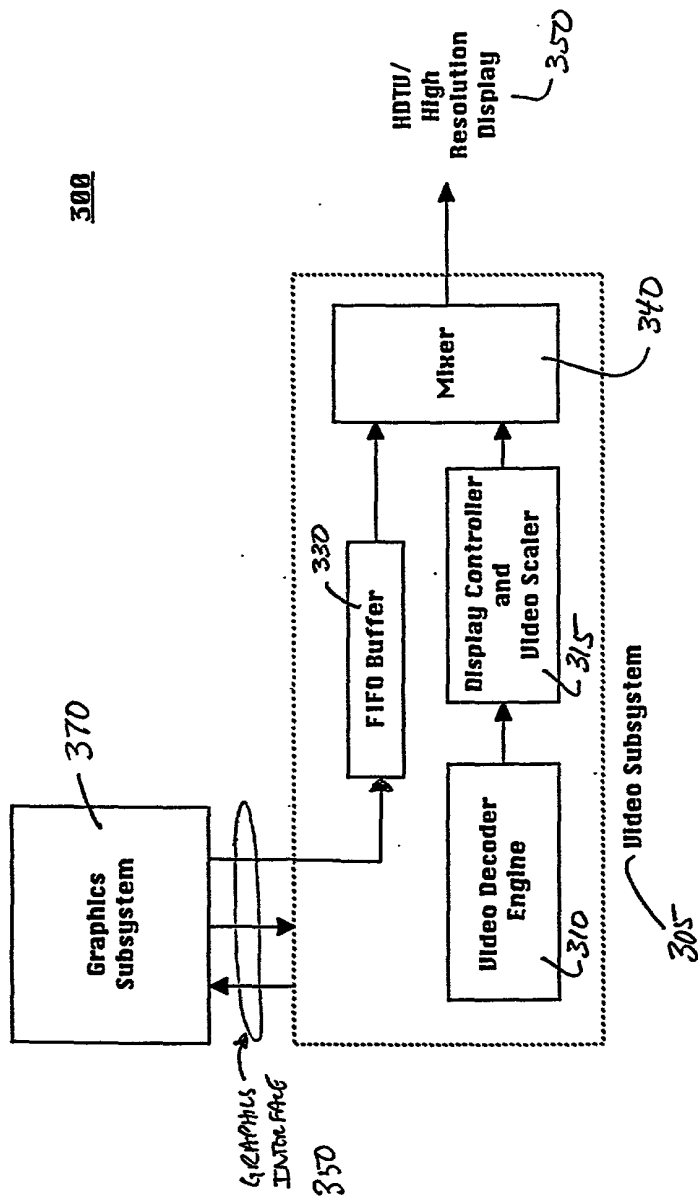
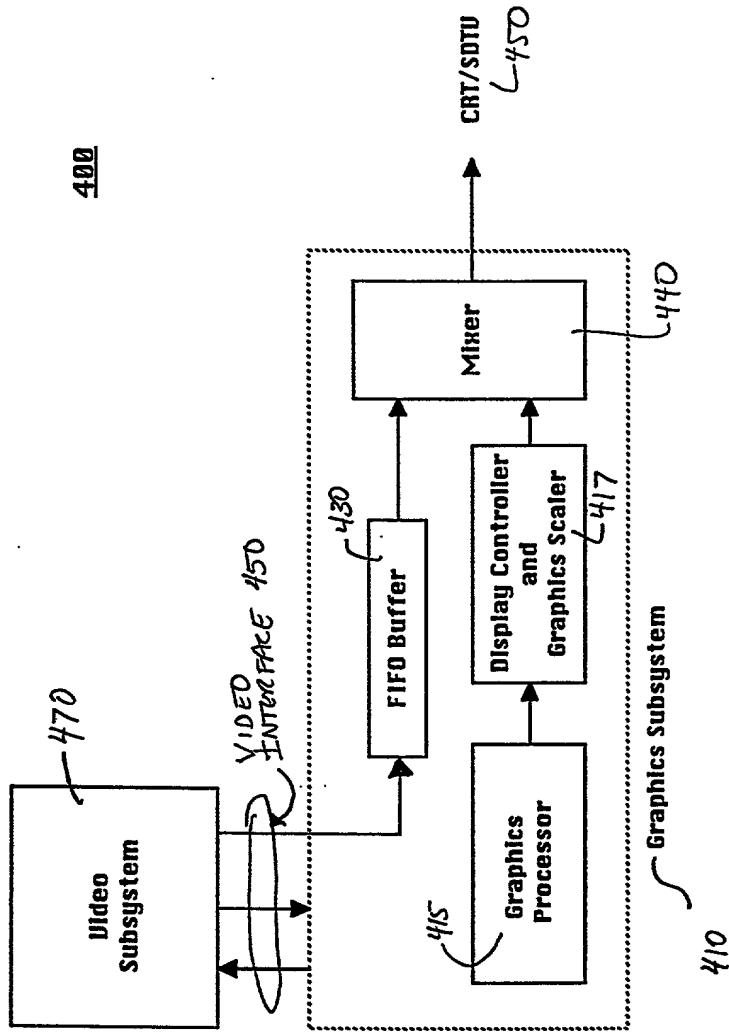
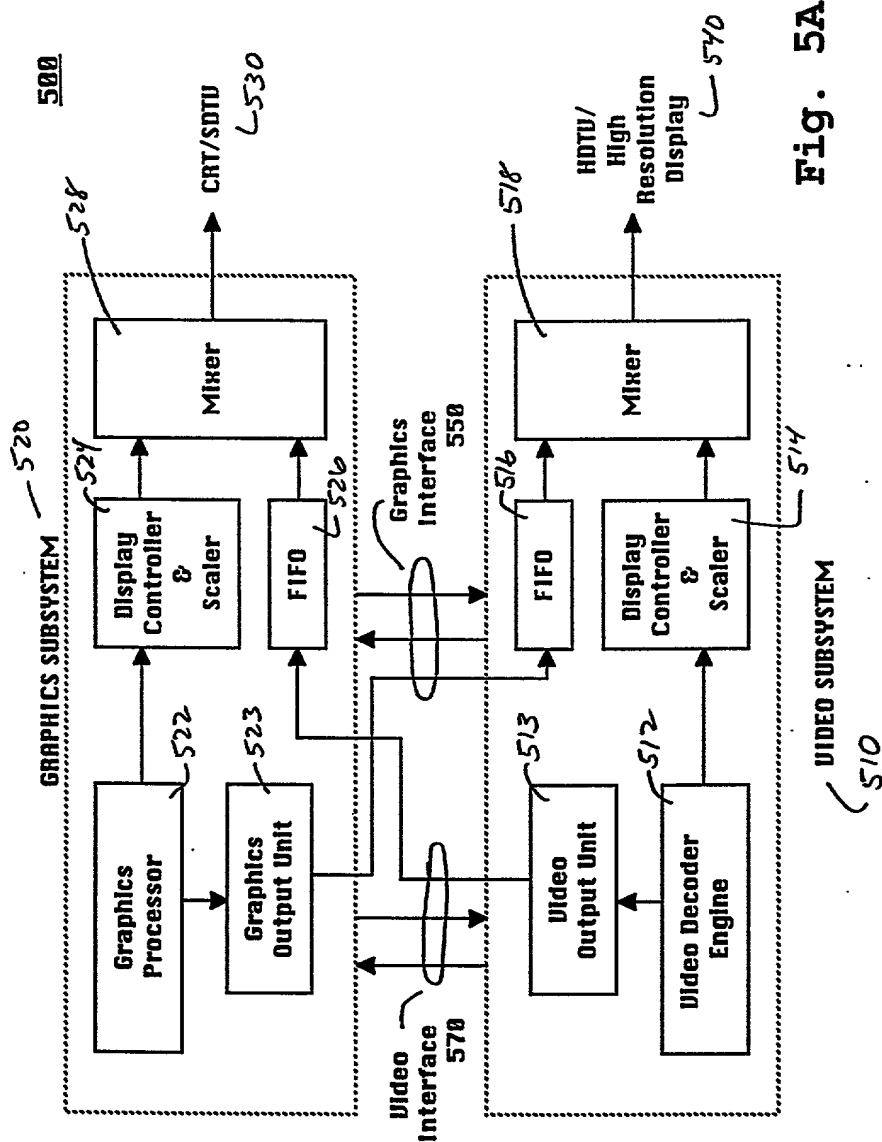


Fig. 3



**Fig. 4**



**Fig. 5A**

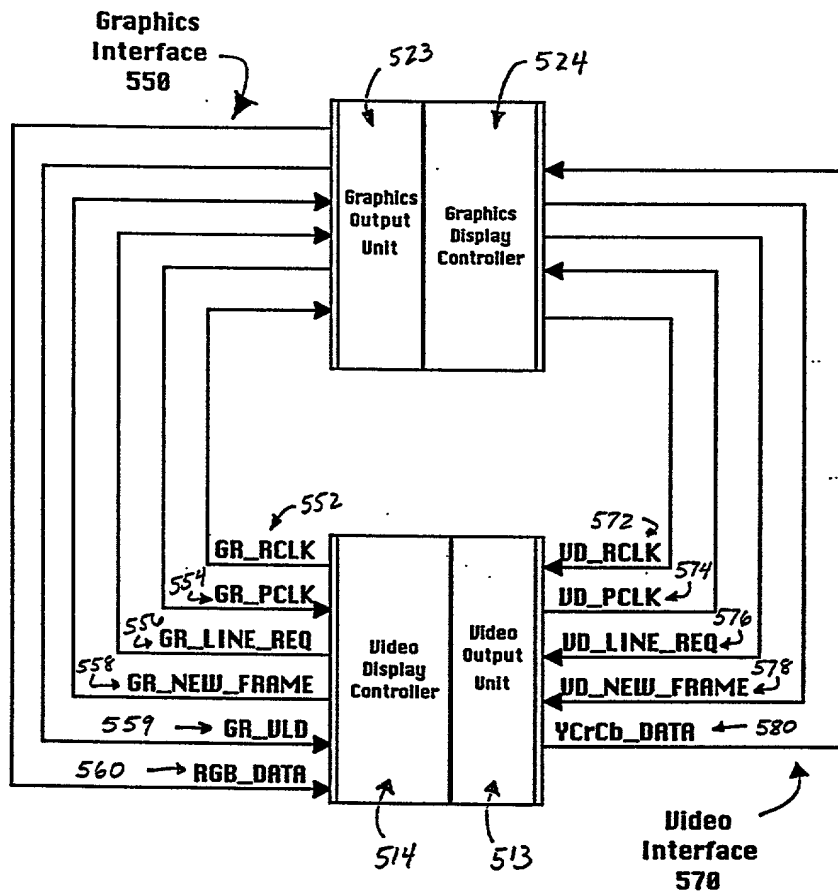


Fig. 5B

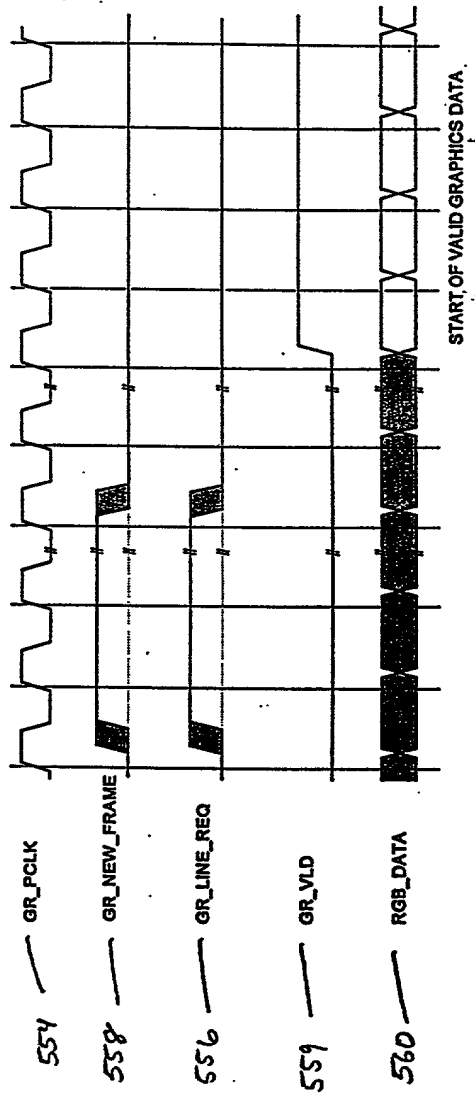


FIG. 6



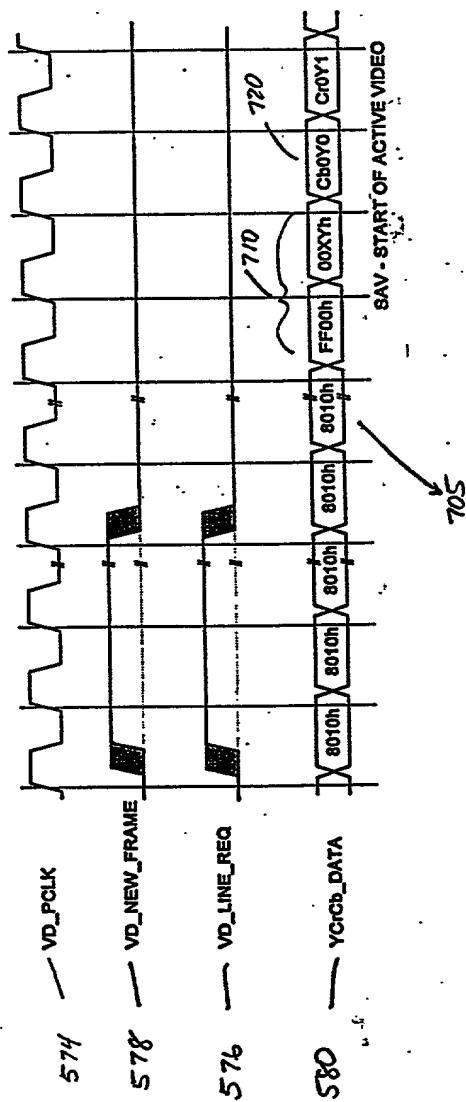
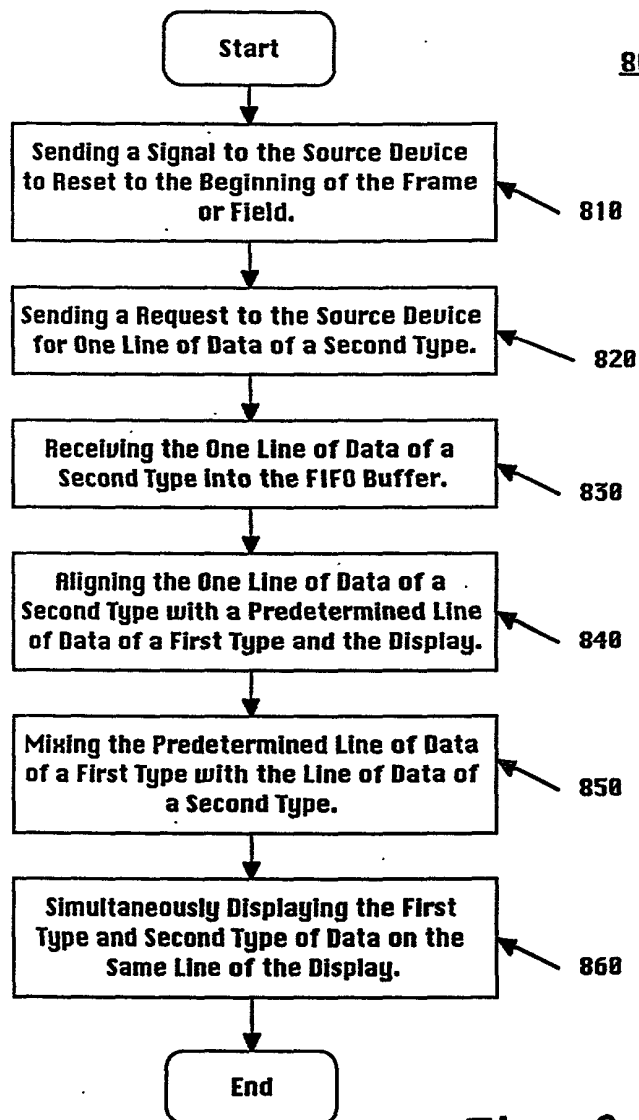


FIG. 7



**Fig. 8**